1 Scope:

1.1 This specification is applicable to fully lead-free and halogen-free FST series thick film chip resistors.

1.2 Fully lead-free products without RoHS exemptions.

1.3 Superior sulfur resistant capability (Refer to ASTM-B-809-95&EIA977 sulfur vapor test).

1.4 The product is for general electronic purpose.

2 Explanation Of Part Numbers:

(EX)

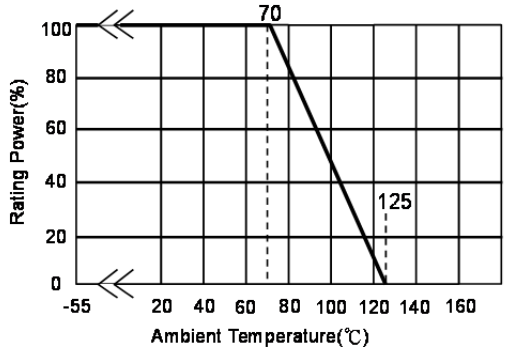
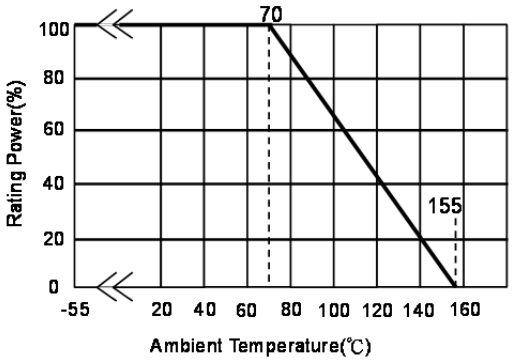
FST 0402 T 100 J B

Type	Size	Packaging	Nominal Resistance		Resistance Tolerance	Fos Test
Fully lead-free Anti-sulfurated Thick Film Chip Resistors	0201 0402 0603 0805 1206 1210 2010 2512	T : Taping	5% (3- Digit)	EX. 10Ω=100 4.7Ω=4R7 JUMPER=000	D=± 0.5% F=± 1% J=± 5%	B=105℃
			0.5% 1% (4-Digit)	EX. 10.2Ω=10R2 10KΩ=1002		

3 General Specifications:

Type	Rated Power at 70℃	Max. Working Voltage	Max. Overload Voltage	T.C.R (ppm/℃)	Resistance Range			JUMPER (0Ω) Rated Current	JUMPER (0Ω) Resistance Value
					D(±0.5%) E-24、E-96	F(±1%) E-24、E-96	J(±5%) E-24	J (±5%)	J (±5%)
FST0201	1/20 W	25V	50V	±200	1Ω≦R≦1MΩ	1Ω≦R≦10MΩ	1Ω≦R≦10MΩ	0.5A	100mΩ MAX
FST0402	1/16 W	50V	100V	±200	1Ω≦R≦2.2MΩ	1Ω≦R≦10MΩ	1Ω≦R≦10MΩ	1A	100mΩ MAX
FST0603	1/10 W	75V	150V	±150	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	1A	100mΩ MAX
				±200	------	2.2MΩ＜R≦10MΩ	2.2MΩ＜R≦10MΩ		
FST0805	1/8 W	150V	300V	±150	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	2A	100mΩ MAX
				±200	------	2.2MΩ＜R≦10MΩ	2.2MΩ＜R≦10MΩ		
FST1206	1/4 W	200V	400V	±150	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	2A	100mΩ MAX
				±200	------	2.2MΩ＜R≦10MΩ	2.2MΩ＜R≦10MΩ		
FST1210	1/2 W	200V	400V	±150	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	2A	100mΩ MAX
				±200	------	2.2MΩ＜R≦10MΩ	2.2MΩ＜R≦10MΩ		
FST2010	3/4 W	200V	400V	±150	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	2A	100mΩ MAX
				±200	------	2.2MΩ＜R≦10MΩ	2.2MΩ＜R≦10MΩ		
FST2512	1W	200V	400V	±150	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	1Ω≦R≦2.2MΩ	2A	100mΩ MAX
				±200	------	2.2MΩ＜R≦10MΩ	2.2MΩ＜R≦10MΩ		
Operating Temperature Range	-55℃ ~ +155℃ (0201:-55℃ ~ +125℃)								

3.1 Power Derating Curve:

Type	FST0201	Other
Operating Temperature Range	－55℃ ~ ＋125℃	－55℃ ~ ＋155℃
Explain	If the ambient temperature exceeds 70 degrees centigrade to 125 degrees centigrade, the power can be modified by the curve as below.	If the ambient temperature exceeds 70 degrees centigrade to 155 degrees centigrade, the power can be modified by the curve as below.
Figure		

3.2 Voltage Rating:

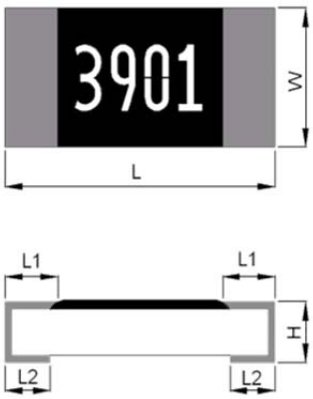
Rated Voltage: DC voltage or AC voltage (rms) based on the rated power.

The voltage can be calculated by the following formula. If the calculated value exceeds the Max voltage specified in the Table 3, the Max voltage rating is set as the voltage rating.

$$E=\sqrt{R\times P}$$

E= Rated voltage(V)
P= Power rating(W)
R= Nominal resistance(Ω)

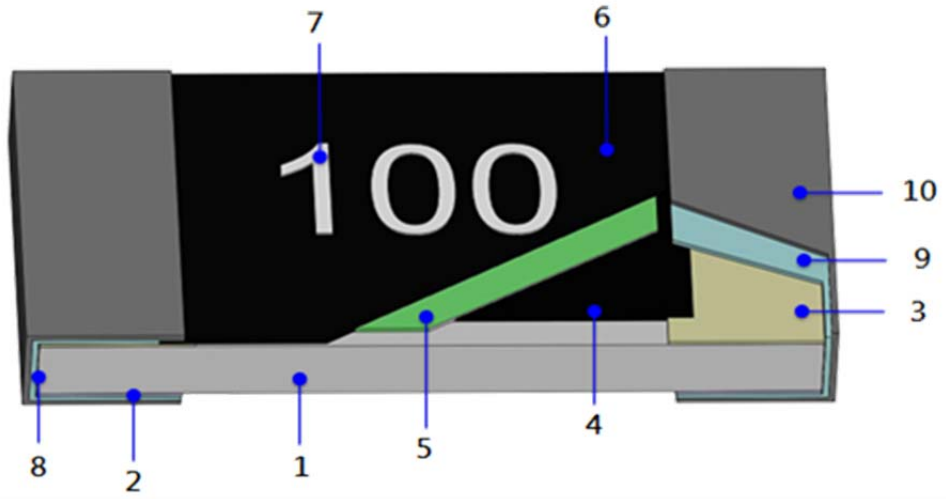
4 Dimensions:



Unit:mm

Type \ Dimension / Size Code	L	W	H	L1	L2
FST 0201	0.60±0.03	0.30±0.03	0.23±0.03	0.10±0.05	0.15±0.05
FST 0402	1.00±0.10	0.50±0.05	0.30±0.05	0.20±0.10	0.25±0.10
FST 0603	1.60±0.10	0.80±0.10	0.45±0.10	0.30±0.15	0.30±0.15
FST 0805	2.00±0.10	1.25±0.10	0.50±0.10	0.35±0.20	0.35±0.15
FST 1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
FST 1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
FST 2010	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20
FST 2512	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20

5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

6 ReliabilityTest:

6.1 Electrical Performance Test

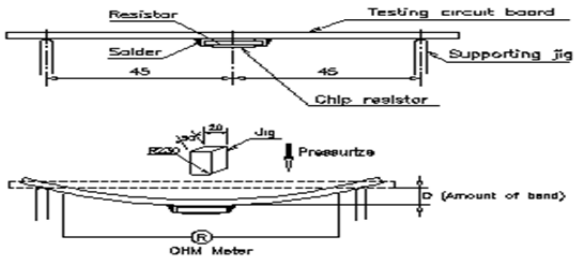
<table>
<tr><th rowspan="2">Item</th><th rowspan="2">Conditions</th><th colspan="2">Specifications</th></tr>
<tr><th>Resistors</th><th>Jumper</th></tr>
<tr><td>Temperature Coefficient of Resistance</td><td>TCR（ppm/℃）$= \frac{(R2-R1)}{R1(T2-T1)} \times 10^6$
R1: Resistance at room temperature
R2: Resistance at -55℃or +125℃
T1: Room temperature
T2: Temperature -55℃or +125℃

Refer to JIS-C5201-1 4.8</td><td>Refer to item 3. general specifications</td><td>NA</td></tr>
<tr><td>Short Time Overload</td><td>Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes , then measure its resistance variance rate.(Rated voltage refer to item 3. general specifications)

Refer to JIS-C5201-1 4.13</td><td>0.5%、1%:
△R%=±1.0%
5%:
△R%=±2.0%</td><td>Refer to item 3. general specifications</td></tr>
<tr><td>Dielectric Withstand Voltage</td><td>Put the resistor in the fixture, add VAC (see SPEC below) in +,- terminal for
FST0201、0402、0603apply 300 VAC 1 minute.
FST0805、1206、1210、2010、2512 apply 500 VAC 1 minute.

Refer to JIS-C5201-1 4.7</td><td colspan="2">No short or burned on the appearance.</td></tr>
<tr><td>Intermittent Overload</td><td>Put the tested resistor in chamber and load 2.5 times rated DC voltage for 1 sec on, 25 sec off, the totalof 10000+400/-0 test cycles, then it be left at no-load for 1 hour , then measure its resistance variance rate.
Jumper : Applied Maximum overload current
<table>
<tr><th>Type / Jumper</th><th>FST0201</th><th>FST0402</th><th>FST0603</th><th>FST0805</th><th>FST1206</th><th>FST1210</th><th>FST2010</th><th>FST2512</th></tr>
<tr><td>±5%</td><td>1.25A</td><td>2.5A</td><td>2.5A</td><td>5A</td><td>5A</td><td>5A</td><td>5A</td><td>5A</td></tr>
<tr><td>±1%</td><td>1.25A</td><td>3.75A</td><td>5A</td><td>6.25A</td><td>8.75A</td><td>10A</td><td>12.5A</td><td>17.5A</td></tr>
</table>
Refer to JIS-C5201-1 4.13</td><td colspan="2">No short or burned on the appearance</td></tr>
</table>

6.2 Mechanical Performance Test

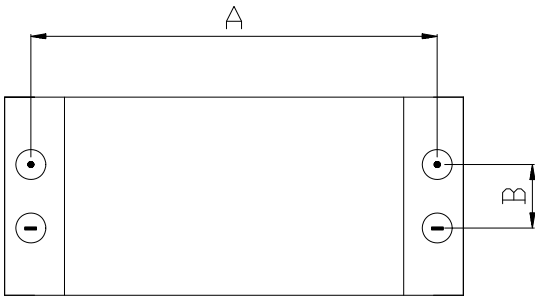
Item	Conditions	Specifications	
		Resistors	Jumper
Solderability	Preconditioning: Put the tested resistor in the apparatus of PCT, at a temperature of 105℃, humidity of 100% RH, and pressure of 1.22×10^5 Pa for a duration of 4 hours. Then after left the tested resistor in room temperature for 2 hours or more. Test method: The resistor be immersed into solder pot in temperature 235±5℃ for 2 sec, then the resistor is left as placed under microscope to observed its solder area. Refer to JIS-C5201-1 4.17	Solder coverage over 95%	
Resistance to Soldering Heat	◎Test method 1 (solder pot test): The tested resistor be immersed into molten solder of 260+5/-0℃ for 10+1/-0 seconds. Then the resistor is left in the room for 1 hour. ◎Test method 2 (solder pot test): The tested resistor be immersed into molten solder of 260+5/-0℃ for 30+1/-0 seconds. Then the resistor is left as placed under microscope to observe its solder area. ◎Test method 3 (Electric iron test): Preheating temperature : 350±10℃ Electric iron preheating time : 3+1/-0 sec Preheating the electric iron on electrode termination, as after that step placed the iron over 60 min. and measured its resistance variance rate. Refer to JIS-C5201-1 4.18	Test item 1: (1).Variance rate on resistance ΔR%=±1.0% Test item 2: (1).Solder coverage over 95%. (2).The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode. Test item 3: (1).Variance rate on resistance ΔR%=±1.0%	Refer to item 3. general specifications
Joint Strength of Solder	◎Bending Strength: Solder tested resistor on to PC board. Add force in the middle down, and under load measured its resistance variance rate. D:FST0402、0603、0805=5mm FST0201、1206、1210=3mm FST2010、2512=2mm  Refer to JIS-C5201-1 4.33	ΔR%=±1.0%	Refer to item 3. general specifications

6.3 Environmental Test

Item	Conditions	Specifications	
		Resistors	Jumper
Resistance to Dry Heat	Put tested resistor in chamber under temperature 155±5℃ for 1000 +48/-0 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. PS:FST0201 for 125±3℃ Refer to JIS-C5201-1 4.25	0.5%、1%: ΔR%=±1.0% 5%: ΔR%=±2.0%	Refer to item 3. general specifications
Thermal Shock	Put chip resistors in the thermal shock machine ,and the temperature was -55℃ for 15 minutes and +125℃ for 15 minutes, the total of 300 times and then removed, let stand for more than 1 hour before measuring the resistance change rate Testing Condition: Lowest Temperature -55±5℃; Highest Temperature 125±5℃; Temperature-retaining time 15 minutes each Refer to MIL-STD 202 Method 107	0.5%、1%: ΔR%=±0.5% 5%: ΔR%=±1.0%	Refer to item 3. general specifications
Loading Life in Moisture	Put the tested resistor in the chamber under temperature 40±2℃, relative humidity 90~95% and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate Refer to JIS-C5201-1 4.24	0.5%、1%: ΔR%=±2.0% 5%: ΔR%=±3.0%	Refer to item 3. general specifications
Load Life	Put the tested resistor in chamber under temperature 70±2℃ and load the rated voltage for 90 minutes on, 30 minutes off, total 1000 hours. Then leaving the tested resistor in room temperature for 60 minutes, and measure its resistance variance rate. Refer to JIS-C5201-1 4.25	0.5%、1%: ΔR%=±2.0% 5%: ΔR%=±3.0%	Refer to item 3.general specifications
Sulfuration Test	Class B: Put the tested resistor in sulfur vapor, at a temperature of 105±2℃for 750hrs Refer to ASTM-B-809-95&EIA977	ΔR=±4.0%	Refer to item 3.general specifications

7 Measurement Point：

Bottom electrode



⊙ Current Terminal

⊖ Voltage Terminal

Unit : mm

DIM / TYPE	A	B
FST0201	0.44±0.05	0.22±0.05
FST0402	0.80±0.05	0.24±0.05
FST0603	1.35±0.05	0.35±0.05
FST0805	1.80±0.05	0.35±0.05
FST1206	2.90±0.05	0.35±0.05
FST1210	2.90±0.05	0.35±0.05
FST2010	4.50±0.05	1.15±0.05
FST2512	5.90±0.05	1.60±0.05

8 Plating Thickness：

8.1 Ni:≧2μm

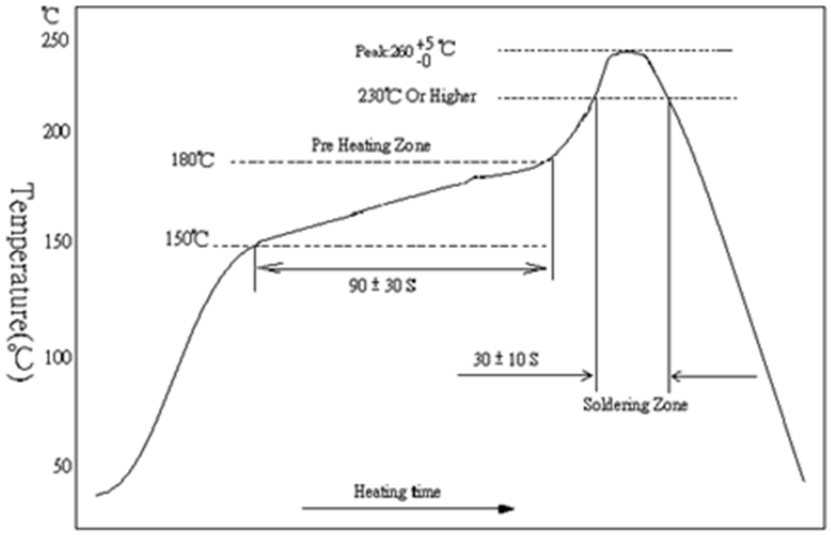
8.2 Sn(Tin):≧3μm

8.3 Sn(Tin):Matte Sn

9 Technical application notes:(This is a recommend ation,please adjust it according to actual application)

9.1 Recommend Soldering Method:

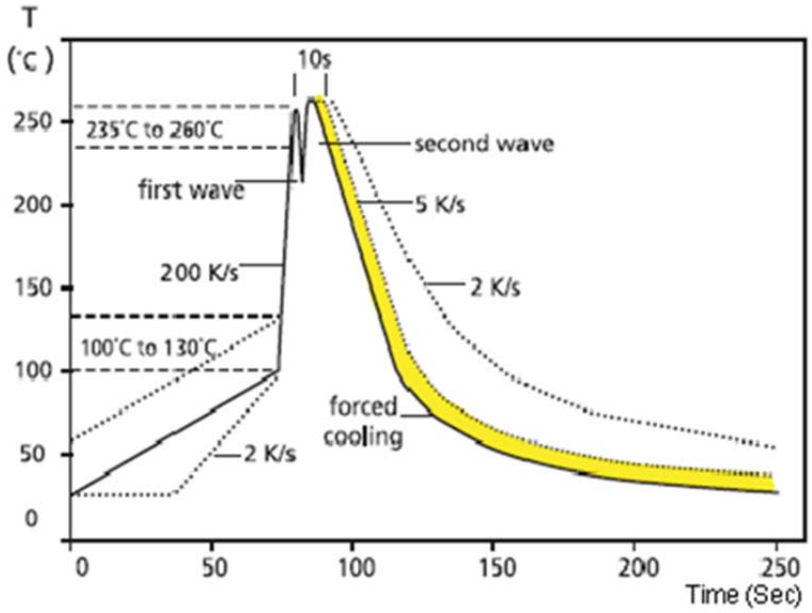
9.1.1 Lead Free IR Reflow Soldering Profile



Remark1:Recommended IR Reflow Soldering Profile meet J-STD-020D.

Remark2:The peak temperature of soldering heat is 260 +5/-0℃ for 10 seconds

9.1.2 Lead Free Double-Wave Soldering Profile(Applicable to products above 0603(inclusive))

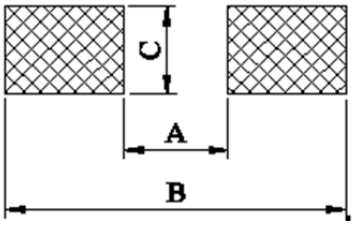


9.1.3 Soldering Iron: temperature 350℃±10℃, dwell time shall be less than 3 sec.

9.2 Land Pattern Design (For Reflow Soldering)：

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

Unit:mm



TYPE \ DIM	A	B	C
FST0201	0.3	1.0	0.4
FST0402	0.5	1.5	0.6
FST0603	0.8	2.1	0.9
FST0805	1.2	3.0	1.3
FST1206	2.2	4.2	1.6
FST1210	2.2	4.2	2.8
FST2010	3.5	6.1	2.8
FST2512	3.8	8.0	3.5

9.3 Environment Precautions:

This specification product is for general electronic use, ABCO will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications, you need to confirm with ABCO.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

(a) Used in high temperature and humidity environment.

(b) Exposed to sea breeze or other corrosive gas, such as Cl2、H2S、NH3、SO2 and NO2.

(c) Used in non-verified liquids including water, oil, chemical and organic solvents.

(d) Using non-verified resin or other coating material to seal or coat our Company product.

(e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

9.4 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving

9.5 Operation and Processing Precautions:

(a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.

(b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.

(c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.

(d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.

(e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

10 Stock period:

10.1The temperature condition must be controlled as 25±5℃, the R.H. must be controlled as 60±15%. The stock can maintain quality level in two years.

10.2Please avoid the mentioned harsh environment below when storing to ensure product performance and its weldability. Places exposed to sea breeze or other corrosive gas, such as Cl2、H2S、NH3、SO2 and NO2.

10.3When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

11 The carton packaged for electronic-information products is made by the symbol as follows: (For china)

	
Marking for control of pollution cause by electronic-information products	Marking for package recovery

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